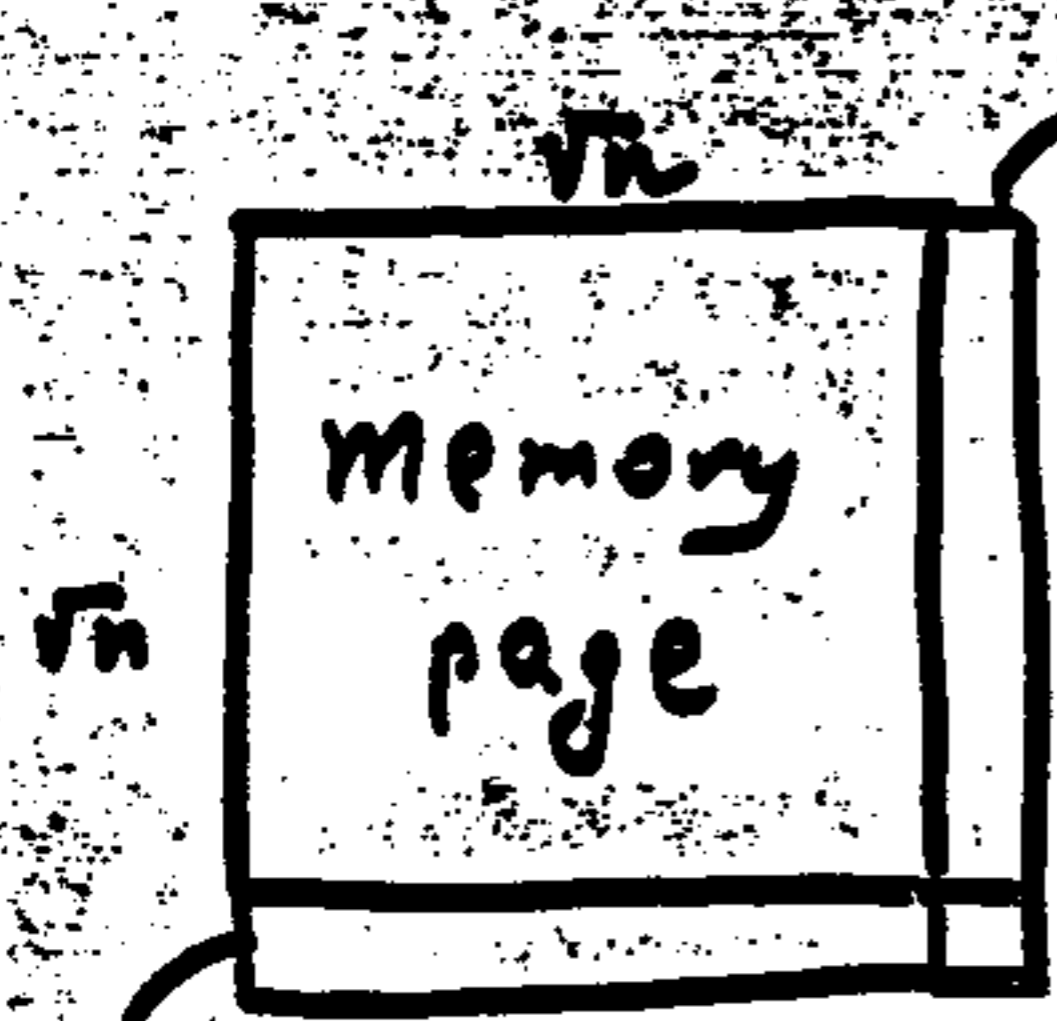


Two-dimensional Parity Checking



Correct: all single errors

Detect: all single double and triple errors

Example

0	1	1	0	0
1	0	1	0	0
0	1	1	1	1
0	1	0	1	0
1	1	1	0	1

check column

$$d(v) = 4$$

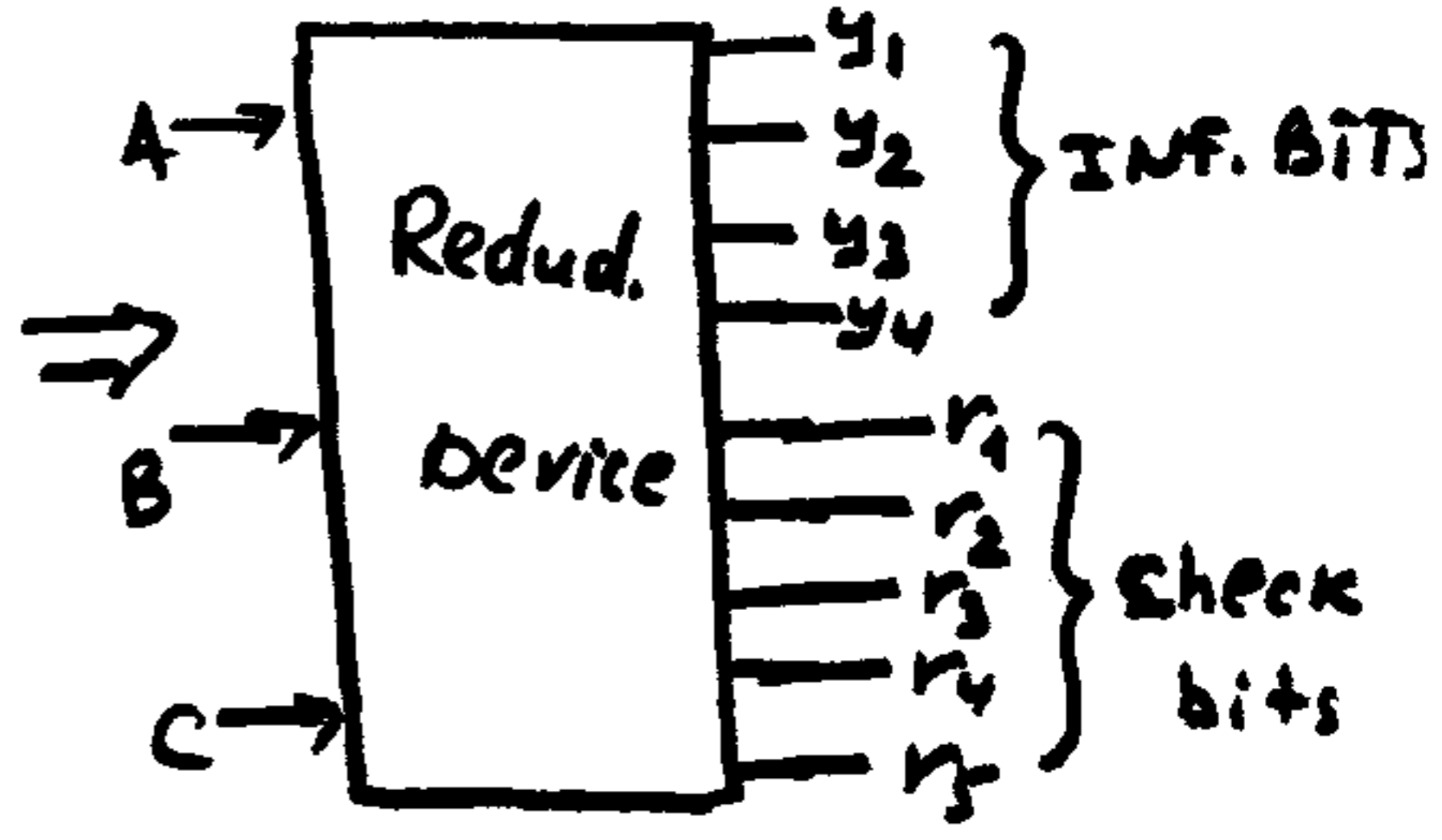
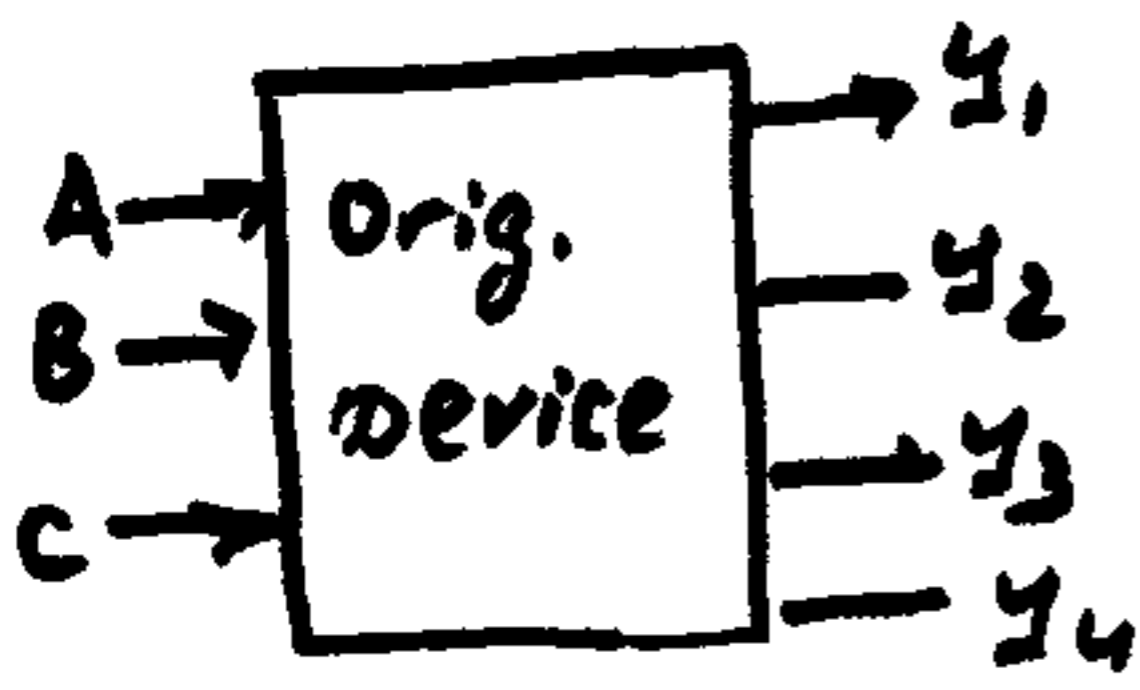
$$n = k + 2\sqrt{k} + 1$$

check row

$$k = 64 \Rightarrow n = 81$$

DETECTION OF SINGLE, DOUBLE AND TRIPLE ERRORS BY TWO-DIMENSIONAL PARITY CHECK CODES.

Example . $k=4$ $n=2\sqrt{k}+1=5$



$y_1 \ y_2$
 $y_3 \ y_4$

\Rightarrow

r_1	r_2	r_3
y_1	y_2	r_4
y_3	y_4	r_5

$r_1 \oplus r_2 \oplus r_3 = 0, \quad r_3 = r_1 \oplus r_2$
 $y_1 \oplus y_2 \oplus r_4 = 0, \quad r_4 = y_1 \oplus y_2$
 $y_3 \oplus y_4 \oplus r_5 = 0, \quad r_5 = y_3 \oplus y_4$

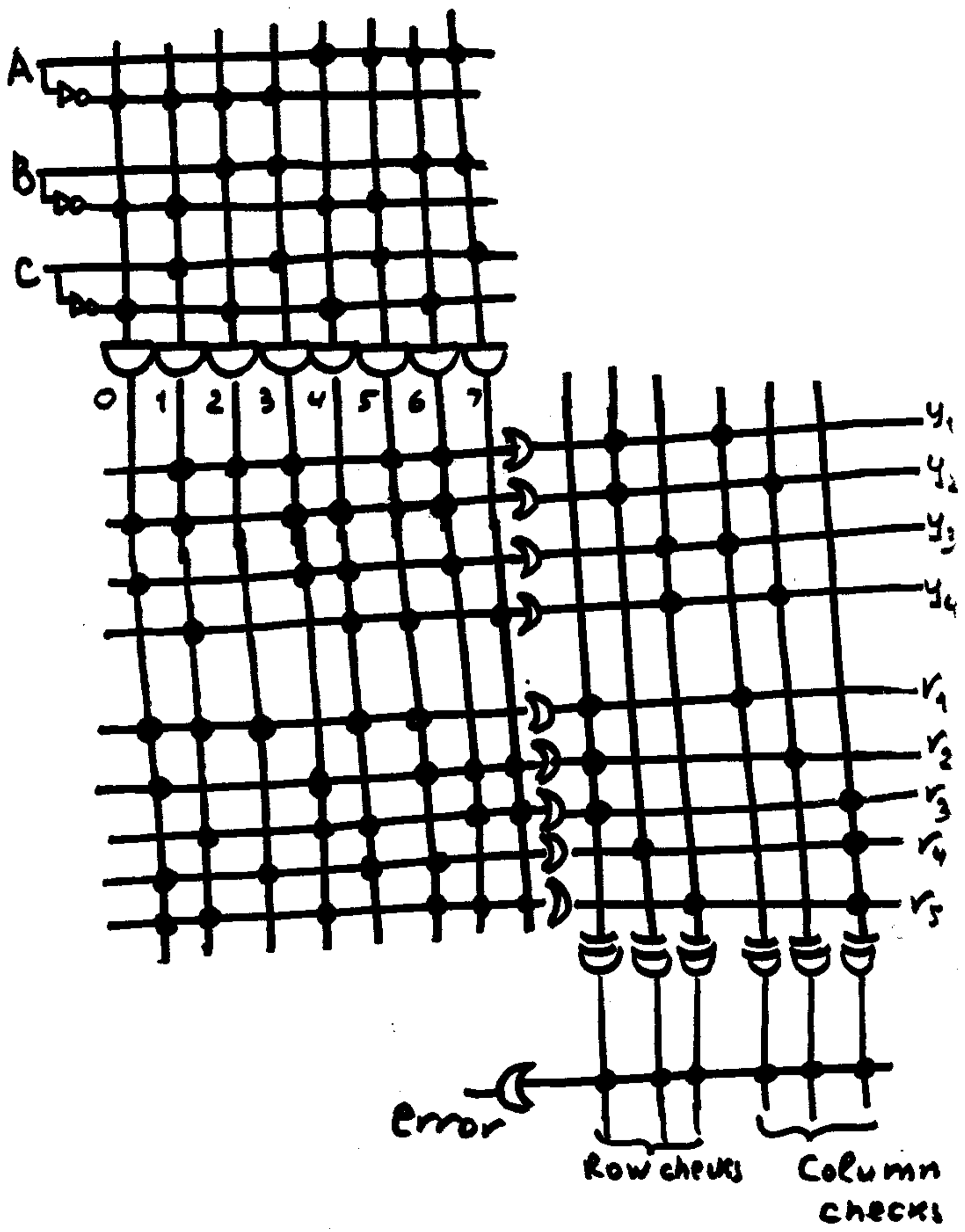
Row checks

$r_1 \oplus y_1 \oplus y_3 = 0, \quad r_1 = y_1 \oplus y_3$
 $r_2 \oplus y_2 \oplus y_4 = 0, \quad r_2 = y_2 \oplus y_4$
 $r_3 \oplus r_4 \oplus r_5 = 0, \quad r_3 = r_4 \oplus r_5$

Column checks

Example (Ctd)

Implementation



ARRAY MULTIPLIER WITH SELF-ERROR-DETECTION by parity prediction

EXAMPLE 4-bit MULTIPLIER

$$P = A * b$$

$$A = (a_3, a_2, a_1, a_0)$$

$$b = (b_3, b_2, b_1, b_0)$$

$$P = (P_7, P_6, \dots, P_0)$$

$$A_c = a_3 \oplus a_2 \oplus a_1 \oplus a_0$$

$$b_c = b_3 \oplus b_2 \oplus b_1 \oplus b_0$$

$$P_c = P_7 \oplus \dots \oplus P_0$$

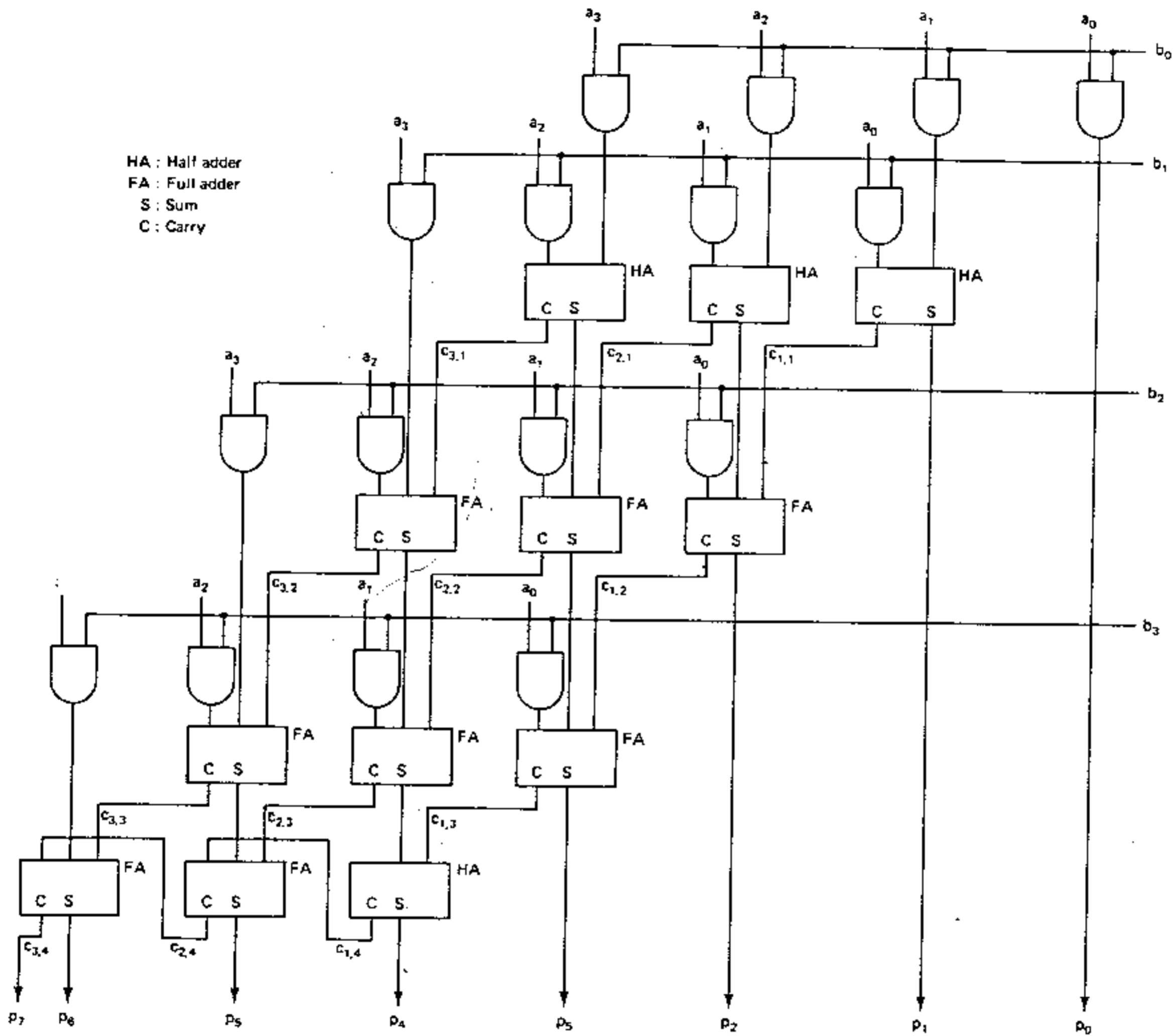
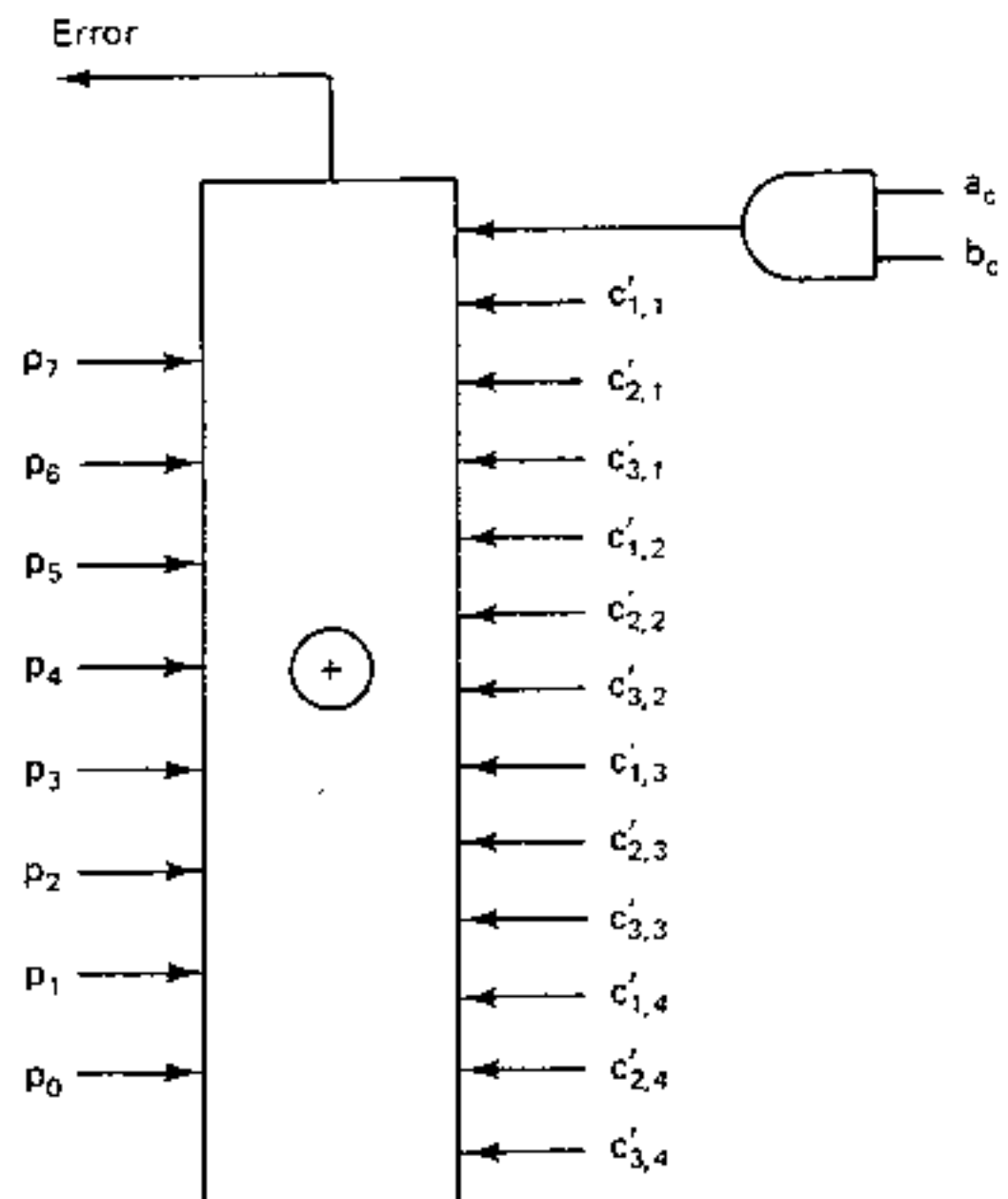


Figure 5.2.6 Multiplier using array of full-half adders.

like $10 \div 10 = 1$. When both the dividend and the divisor are positive, the sign bits are 0.

Each cell, denoted by $CAS(i, j)$, $i, j = 0, 1, 2, 3$, is a complete adder, with the detailed structure illustrated in Fig. 5.2.6.



(b)

Figure 5.2.7 (cont.)

~~Fig. 5.2.8, parity generator circuit~~
1 Δ

$$P_0 = a_0 b_0$$

$$P_1 = a_0 b_1 \oplus a_1 b_0$$

$$P_2 = a_0 b_2 \oplus a_1 b_1 \oplus a_2 b_0 \oplus C_{1,1}$$

$$P_3 = a_0 b_3 \oplus a_1 b_2 \oplus a_2 b_1 \oplus a_3 b_0 \oplus C_{2,1} \oplus C_{1,2}$$

$$P_4 = a_1 b_3 \oplus a_2 b_2 \oplus a_3 b_1 \oplus C_{3,1} \oplus C_{2,2} \oplus C_{1,2}$$

$$P_5 = a_2 b_3 \oplus a_3 b_2 \oplus C_{3,2} \oplus C_{2,3} \oplus C_{1,4}$$

$$P_6 = a_3 b_3 \oplus C_{3,3} \oplus C_{2,4}$$

$$P_7 = C_{3,4}$$

$$P_c = \bigoplus_{i=0}^7 P_i = \left(\bigoplus_{i=0}^3 a_i \right) \left(\bigoplus_{i=0}^3 b_i \right) \oplus$$

$$\bigoplus_{i=1}^3 \bigoplus_{j=1}^4 C_{i,j} = a_c b_c \oplus \bigoplus_{i=1}^3 \bigoplus_{j=1}^4 C_{i,j}$$

100

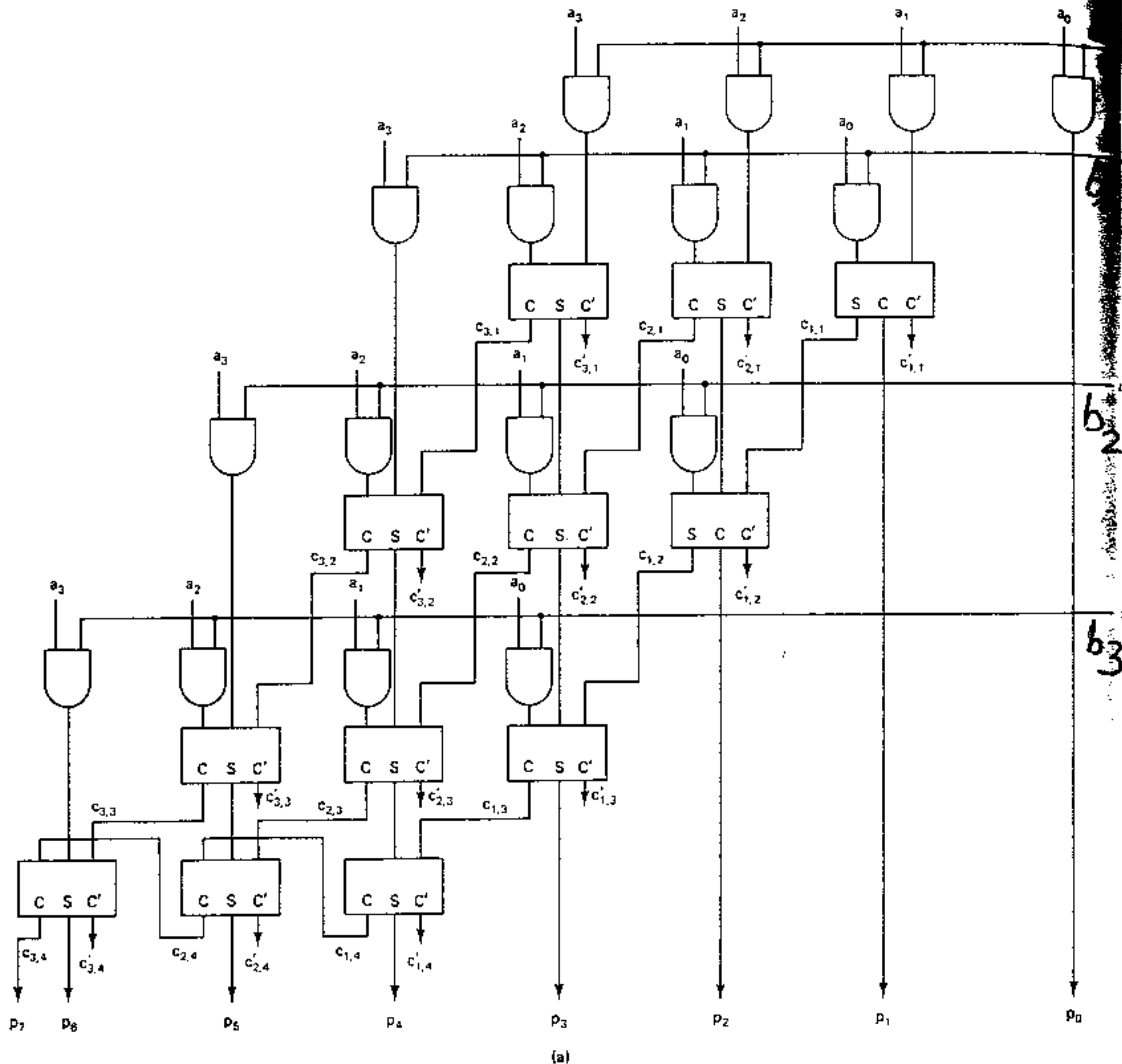


Figure 5.2.7 Parity-checked multiplier.

The row of x_i 's in $0, 1, \dots, n-1$ contains a product $x_i y_j$ of x_i and the leftmost bit of y , the sign bit of y , and satisfies the following relation:

Transmission Rates

101 (5)

k - number of information bits

n - number of bits in an expanded word

r - number of redundant bits

$$n = k + r$$

Transmission rate for an (n, k) -code

$$R = \frac{k}{n} \quad 0 < R < 1$$

For one-dimensional parity:

$$R = \frac{n-1}{n} = 1 - \frac{1}{n} \rightarrow 1 \text{ for large } n$$

For two dimensional parity:

$$R = \frac{k}{k + 2\sqrt{k} + 1} \rightarrow 1 \text{ for large } k$$