

Spectral Techniques in Binary and Multiple-Valued Switching Theory

A review of results in the decade 1991-2000

Mark G. Karpovsky
Dept. of Electlectrical and
Computer Engineering
Boston University
8 Saint Mary's Street
Boston, Ma 02215, USA

Radomir S. Stanković
Dept. of Computer Science
Faculty of Electronics
Beogradska 14
18 000 Niš
Yugoslavia

Claudio Moraga
Dept. of Computer Science and
Computer Engineering
Dortmund University
44221 Dortmund
Germany

Abstract

This paper presents a tutorial review of spectral methods in switching and multiple-valued logic theory and the design of digital system in the last decade. The paper continues review work in this area done by the authors in 1981 and 1991.

1 Introduction

Spectral techniques are a mathematical discipline in the area of abstract harmonic analysis devoted to the applications in engineering, primarily electrical and computer engineering. Practical applications of spectral techniques in analysis and design with switching and multiple-valued (MV) functions date back to the early history of these areas [6], [11], [23], [39], [43], [52], [63], [91], while mathematics foundation are set even earlier [104]. It is believed that spectral techniques have developed into a separate discipline during the probably highest activity in this area between 1970 and 1975, which is possibly best expressed by pointing out annual symposia on Walsh and other nonsinusoidal functions, with even two international meetings on that subject in 1973 and 1975. After that time, although with somewhat fluctuating interest, spectral techniques have been continuously developing and activity in the area has been summarized at the international Workshops in Boston, 1985 [33], Montreal, 1986, and Dortmund, 1988 [53], and presented at regular sessions at some conferences on signal processing and circuit design, among which the ISMVL are probably the most supporting for the research in this area.

That activity was discussed and a summary of development in theory and practice of spectral techniques

up to the late seventies was given by Karpovsky [32], while the next decade was overviewed by Moraga [54]. The present paper is an attempt to point out and briefly review some, from our point of view, interesting and important results in spectral techniques in the last decade, without pretending to provide a full review of a rather considerable amount of achievements. The selected references should serve as an illustration of the related statements, and could hardly be considered as a complete bibliography on the subject.

2 Activity in Spectral Techniques

There is apparent a renewed and considerable interest in spectral techniques after the publication of a report about applications of Walsh functions to technology mapping [97]. This interest is due to the development of technology of digital circuits imposing requirements and strong demands regarding complexity and performances of logic networks and digital devices in general, which cannot be met by traditional approaches.

In last decade, the 4th International Workshop on Spectral Techniques was organized in 1990 within the Int. Conf. on Signal Processing, Beijing'90, followed by the 5th Workshop hosted by the Beijing University of Aeronautics and Astronautics, in 1994. The Workshop had an especial support from Volkswagen Foundation of Germany. Selected papers were later collected in [56]. We want to point out as an especially important contribution a bibliography of selected papers in this area published in Russian, prepared by Shmerko and Mikhailov [73].

In 1992, the Workshop on Logic Design and Microprocessor Architecture in Iizuka, Fukuoka, Japan, provided space for a tutorial discussion of spectral methods in logic design by Varma and Trachtenberg, further de-

veloped in a chapter [95] of a book edited afterwards by Sasao [67], accompanied by a chapter on minimization of AND-EXOR expressions including fixed polarity Reed-Muller expressions [66].

It should be noted that renewed interest in Reed-Muller expressions is due to the publication of the paper [70] presenting a conjecture that AND-EXOR expressions require on the average a fewer number of product compared to SOP expressions. That conjecture was confirmed and experimentally verified by the same authors, and many others in a series of publications. (See a discussion and references in [68], [69]). That renewed interest resulted in organization of Int. Workshops on Application of Reed-Muller Expressions in Circuit Design in Hamburg, Germany, 1993, Chiba, Japan, 1995, Oxford, UK, 1997, and Victoria, Canada, 1999. Besides many other interesting results, an especially important achievement of the Reed-Muller Workshops was the establishment of relationships between spectral techniques and decision diagrams (DDs) as a data structure for representation of large discrete functions. DD methods for calculation of spectral transforms originating in [9] considerably improved applicability of spectral techniques, since permit to overcome problems related to the exponential complexity of FFT algorithms in terms of both space and time. Conversely, spectral interpretation of DDs [75], [78], [85], permits a unified consideration and classification of different DDs [84], and offers a way for further generalizations and optimization of DD representations [21], [27], [77]. It should be noted that publication of a book collecting a selection of papers presented at Reed-Muller Workshop in 1995, edited by Sasao and Fujita [72], is considered of crucial importance for further development of both DD representations and spectral techniques.

Besides the Walsh and Reed-Muller transforms, traditional approaches in spectral techniques are related to the arithmetic and Haar transforms. The Conference Series "Advances in Computer Systems" organized at the Technical University of Szczecin, Poland, has acted as a forum for discussing spectral techniques, and in particular arithmetic transforms, mainly due to the previous background work in this area done in East Europe and Russia. Publication of a monograph [46], a book chapter [30], a tutorial paper [22], and a historic overview [18] are setting fundamentals for further work in this area. Importance of arithmetic expressions is further raised by showing that some classes of decision diagrams represent functions in the form of arithmetic polynomials [75], [78], [83], [85].

For a recent renewed interest and development in Haar transforms [29], [31], credit should be given to the

organization of a special session of spectral techniques and DDs, accompanied by another session devoted exclusively to Haar transforms within the Int. Conference on Informatics, Communications and Signal Processing (1st ICICS), 1997, in Singapore.

In two Workshops on Spectral Techniques and Filter Banks, in Tampere, Finland, 1998, and Brandenburg, Germany, 1999, spectral techniques for switching theory and logic design were considered within the spectral methods for signal processing. That work motivated organization of the TICSP Workshop on Spectral Transforms and Logic Design for Future Digital Systems, SPECLOG-2000, hosted by the Tampere Int. Center for Signal Processing, Tampere, Finland.

Besides organization of several Workshops and editing related Proceedings, the publication of a few monographs devoted completely or in part to spectral methods in switching and MV logic should be considered as a considerable support for future work in the area [1], [16], [41], [46], [53], [56], [72], [78], [82], [88], [89], [90], [101], [102], [103].

2.1 Research Interest

Topics of research interest in spectral techniques in the last decade can be briefly summarized as follows

1. Definition of new transforms, [13], [14], [15], [58], [59], [61],
2. Consideration of relationships among the various transforms and related operators, [17], [55], [76], [98], [100],
3. Classical and DD methods for calculation of spectral transforms and related operators, [9], [10], [28], [51], [80], [81], [86],
4. Spectral method for synthesis including testing, [3], [4], [8], [24], [26], [34], [35], [36], [40], [44], [45], [57], [65], [71], [92], [93],
5. Spectral interpretation of DDs and related topics, [7], [12], [19], [78], [83], [85].
6. Definition of new decision diagrams and their application in circuit synthesis, [21], [25], [27], [42], [49], [50], [87], [88], [96],
7. Circuit synthesis for realization of spectral transforms, [2], [47], [48], [60], [62], [64], [98], [99],
8. Historic remarks and corrections in interpretation and applications of spectral methods [18], [79].

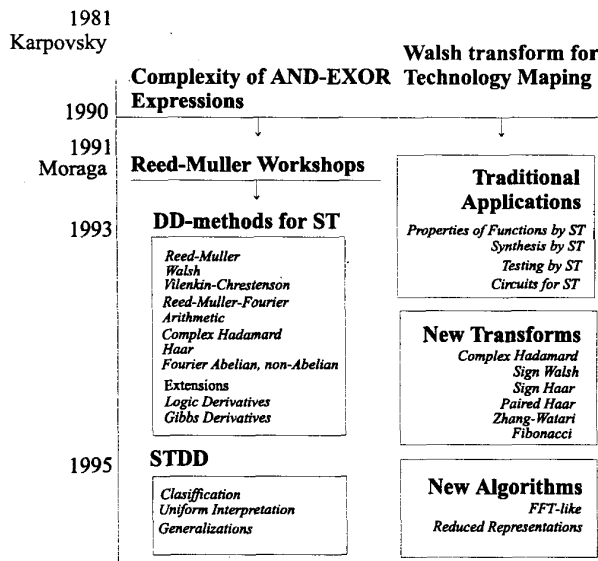


Figure 1. Research subjects.

Fig. 1 shows the timeline and summarizes research activity in the area.

Due to pages limitation is not possible to give here a more complete presentation. Alone the list of references is already 8 pages long. Readers interested in a full version of this work including a discussion of spectral interpretation of DDs, calculation of spectral transforms through DDs, and related applications of spectral techniques on finite not necessarily Abelian groups, may obtain an electronic version by sending a mail to moraga@cs.uni-dortmund.de.

3 Closing Remarks

Spectral techniques allow derivation of alternative methods for solving complex tasks efficiently in terms of space and time. Transferring a problem from the original into the spectral domain may provide several advantages. In particular, some numerical calculation tasks difficult to perform in the original domain, may be simple in the spectral domain. The convolution product, that is often used in description and mathematical modeling of linear shift-invariant systems, is a supporting example. Some properties of a signal or a system, which are shadowed or difficult to observe in the original domain, become easy observable in the spectral domain. Examples are determination of cut-off frequencies and sampling rates in signal processing, and detection of decomposability and symmetry properties in logic design, see for example, [1], [31], [56], [57]. Fast calculation algorithms for spec-

tral coefficients further improve performances of related methods. Decision diagrams (DDs) extends applicability of these algorithms to functions defined in a large number of points, [9].

The complexity of problems in switching and MV theory and logic design give rise for further application of spectral techniques, since they may provide for simple and elegant analytic solutions where the traditional approaches reduce to the brute force search methods.

Acknowledgements

Special thanks are due to Prof. Jaakko T. Astola of Tampere Int. Center for Signal Processing (TICSP), Tampere, Finland for providing an opportunity for the authors to meet at TICSP and for comments and suggestions in several discussions which were very enlightening and helpful for the work leading to this report.

References

- [1] Aghaian, S., Astola, J., Egiazarian, K., *Binary Polynomial Transforms and Nonlinear Digital Filters*, Marcel Dekker, 1995.
- [2] Antonenko, V., Ivanov, A., Shmerko, V.P., "Linear arithmetic forms of k -valued logic functions and their realization on systolic arrays", *Automation and Remote Control*, Vol. 56, No. 3, Pt. 2, 1995, 419-432.
- [3] Benett, L.A.M., "Synthesis of multioutput logic networks using spectral techniques", *IEE Proc. Computers, Digit. Techn.*, Vol. 142, No. 4, 1995, 241-248.
- [4] Bernasconi, A., Codendotti, B., "Spectral analysis of Boolean functions as a graph eigenvalue problem", *IEEE Trans. Comput.*, Vol. 48, No.3, 1999, 345-351.
- [5] Bryant, R.E., Chen, Y.-A., "Verification of arithmetic functions with binary moment decision diagrams", Research Report CMU-CS-94-160, May 31, 1994.
- [6] Calingaert, P., "Switching function canonical form based on commutative and associative binary operations", *Trans. Amer. Inst. Elect. Eng.*, Part I, Vol. 79, January 1961, 808-814.
- [7] Chang, C.H., Falkowski, B.J., "Generation of quasi-optimal FBDDs through paired Haar spectra", *Proc. of IEEE Int. Symp. on Circuits and Systems*, (31st ISCAS), Monterey, California, USA, Vol. VI, June 1998, 167-170.
- [8] Chattopadhyay, S., Roy, S., Palchaudri, P., "KGPMAP: library-based technology mapping technique for antifuse based FPGA's", *IEE Proc. Computer, Digit. Technique.*, 1994, Vol. 4, No. 6, 361-368.
- [9] Clarke, E. M., M.C., Millan, K.L., Zhao, X., Fujita, M., "Spectral transforms for extremely large Boolean functions", in Kobschull, U., Schubert, E., Rosenstiel, W., Eds., *Proc. IFIP WG 10.5 Workshop on Applications of the Reed-Muller Expression in Circuit Design*, Hamburg, Germany, September 16-17, 1993, 86-90.

- [10] Clarke, E.M., Fujita, M., Zhao, X., "Multi-terminal decision diagrams and hybrid decision diagrams", in: [72], 93-108.
- [11] Cohn, M., *Switching Function Canonical Form over Integer Fields*, Ph. D. thesis, Harvard University, Cambridge, Mass. December 1960.
- [12] Drechsler, R., Stanković, R.S., Sasao, T., "Spectral transforms and Word-level decision diagrams", *Proc. 6th Workshop on Synthesis and System Integration of Mixed Technologies, SASIMI-97*, Osaka, Japan, December 1-2, 1997, 39-44.
- [13] Dubrova, E.V., "Evaluation of m -valued fixed-polarity generalized Reed-Muller canonical forms", *Proc. 29th Int. Symp. on Multiple-Valued Logic*, Freiburg, Germany, 1999, 92-98.
- [14] Dubrova, E.V., Muzio, J.C., "Generalized Reed-Muller canonical form for a multiple-valued algebra", *Multi-Valued Logic Jr.*, 1996, Vol. 1, 65-84.
- [15] Egiazarian, K., Astola, J., "On generalized Fibonacci cubes and unitary transforms", *Applicable Algebra in Engineering, Communication and Computing*, Vol. AAEC 8, 1997, 371-377.
- [16] Endow, Y., *Walsh Analysis*, Tokyo Denki University Press, 1993.
- [17] Falkowski, B.J., "Relations between arithmetic and Haar wavelet transform in form of layered Kronecker matrices", *Electronics Letters*, Vol. 35, No. 10, 1999, 799-800.
- [18] Falkowski, B.J., "A note on the polynomial form of Boolean functions and related topics", *IEEE Trans. on Computers*, Vol. 48, No. 8, 1999, 860-864.
- [19] Falkowski, B.J., Chang, C.H., "Forward and inverse transformations between Haar spectra and ordered binary decision diagrams of Boolean functions", *IEEE Trans. on Computers*, Vol. 46, No. 11, 1997, 1272-1279.
- [20] Falkowski, B.J., Chang, C.H., "Paired Haar spectra computation through operations on disjoint cubes", *IEE Proc. Circuits, Devices and Systems*, Vol. 146, No. 3, 1999, 117-123.
- [21] Falkowski, B.J., Rahardja, S., "Complex spectral decision diagrams", *Proc. 26th Int. Symp. on Multiple-Valued Logic*, May 29-31, 1996, Santiago de Compostela, Spain, 255-260.
- [22] Falkowski, B.J., Shmerko, V.P., Yanuskhevich, S.N., "Arithmetic logic - its status and achievements", *Proc. 4th Int. Conf. Application of Computer Systems, ACS'97*, Szczecin, Poland, November 1997, 208-223.
- [23] Garaev, M.U., Faradzhev, R.G., "On an analog of Fourier expressions over Galois fields and its applications to problems of generalized sequential machines", *Izv. Akad. Nauk Aizerb. SSR, Ser. Fiz.-Techn, i Mat. Nauk*, No. 6, 1968, 69-75.
- [24] Gil, C., Ortega, J., "Parallel test generation using circuit partitioning and spectral techniques", *Proc. 6th Euromicro Workshop on Parallel and Distributed Processing*, January 21-23, 1998, 264-270.
- [25] Günther, W., Drechsler, R., "Efficient manipulation algorithms for linearly transformed BDDs", *Proc. 4th Int. Workshop on Applications of Reed-Muller Expansion in Circuit Design*, Victoria, Canada, May 20-21, 1999, 225-232.
- [26] Hansen, J.P., Sekine, M., "Synthesis by spectral transformation using Boolean decision diagrams", *Proc. 33th ACM/IEEE Design Automation Conf.*, Las Vegas, NV, 1996, 248-253.
- [27] Hansen, J.P., Sekine, M., "Decision diagrams based techniques for the Haar wavelet transform", *Proc. IEEE Int. Conf. on Information, Communications and Signal Processing (1st ICICS)*, Singapore, Vol. 1, September 1997, 59-63.
- [28] Harking, B., Moraga, C., "Efficient derivation of Reed-Muller expansions in multiple-valued logic systems", *Proc. 22nd Int. Symp. on Multiple-Valued Logic*, Sendai, Japan, May 27-29, 1992, 436-441.
- [29] Hurst, S.L., "The Haar transform in digital network synthesis", *Proc. 11th Int. Symp. on Multiple-valued Logic*, Oklahoma, USA, May 1981, 10-18.
- [30] Jain, J., "Arithmetic transform of Boolean functions", in [72], 133-161.
- [31] Karpovsky, M.G., *Finite Orthogonal Series in the Design of Digital Devices*, John Wiley, 1976.
- [32] Karpovsky, M.G., "Spectral methods for decomposition, design and testing of multiple-valued logical networks", *Proc. 11th Int. Symp. on Multiple-Valued Logic*, Oklahoma, USA, May 1981, 1-10.
- [33] Karpovsky, M.G., *Spectral Techniques and Fault Detection*, Academic Press, 1985.
- [34] Karpovsky, M.G., "Spectral techniques for off-line testing and diagnosis of complex systems", *Proc. 5th Int. Workshop on Spectral Techniques*, Beijing, China, March 15-17, 1994, 8-18.
- [35] Karpovsky, M.G., Roziner, T., Moraga, C., "Error detection in multiprocessor systems and array processors," *IEEE Trans. on Computers*, Vol. 44, No. 3, March 1995, 383-394.
- [36] Karpovsky, M.G., Stanković, R.S., Astola, J.T., "Spectral techniques for design and testing of computer hardware", *Proc. TICSP Workshop on Spectral Transforms and Logic Design for Future Digital Systems, SPECLOG-2000*, Tampere, Finland, June 2-3, 2000, 1-34.
- [37] Karpovsky, M.G., Trachtenberg, E.A., "Fourier transforms over finite groups for error detection and error correction in computation channels", *Inf. and Control*, 1979, 40, 335-358.
- [38] Kebschull, U., Schubert, E., Rosenstiel, W., "Multilevel logic synthesis based on functional decision diagrams", *EDAC 92*, 1992, 43-47.
- [39] Komamiya, Y., "Theory of relay networks for the transformation between the decimal and binary system", *Bull. of E.T.L.*, Vol. 15, No. 8, August 1951, 188-197.
- [40] Kondratiev, V.N., Shalyto, A.A., "Realization of Boolean functions by a single arithmetic polynomial with masking", *Avtomatika i Telemekhanika*, 1996, 158-171.

- [41] Kukharev, G.A., Shmerko, V.P., Yanushkevich, S.N., *Technique of Binary Data Parallel Processing for VLSI*, Vysheyskaja shkola, Minsk, 1991, Belaruss.
- [42] Lai, Y.-T., Pedram, M., Vrudhula, S.B.K., "EVBDD-based algorithms for integer linear programming, spectral transformation, and functional decomposition", *IEEE Trans. on CAD*, Vol. 13, No. 8, 1994, 959-975.
- [43] Lechner, R.J., "A transform approach to logic design", *Proc. 9th Symp. Switching and Automata Theory*, 1968, 213-214, (Also *IEEE Trans. on Computers*, Vol. C-19, 1970, 627-640).
- [44] Macii, E., Poncino, M., "Predicting the functional complexity of combinatorial circuits by symbolic spectral analysis of Boolean functions", *Proc. EURO-DAC'95*, 294-299.
- [45] Macii, E., Poncino, M., "Using symbolic Rademacher-Walsh spectral transforms to evaluate the agreement between Boolean functions", *IEE Proc. Comput., Digit. Techn.*, Vol. 143, No. 1, 1996, 64-68.
- [46] Malyugin, V.D., *Paralleled Calculations by Means of Arithmetic Polynomials*, Physical and Mathematical Publishing Company, Russian Academy of Sciences, Moscow, 1997, (in Russian).
- [47] Malyugin, V.D., Stanković, M., Stanković, R.S., "Systolic realizations of the discrete Haar transform", *Automatika i Telemekhanika*, No. 9, 1997, 138-145.
- [48] Megson, G.M., "Systolic arrays for the Haar transform", *IEE Proc. Computers, Digit. Tech.*, Vol. 145, No. 6, 1998, 403-410.
- [49] Meinel, Ch., Somenzi, F., Theobald, T., "Linear shifting of decision diagrams and its application in synthesis", *IEEE Trans. CAD*, Vol. 19, No. 5, 2000, 521-533.
- [50] Miller, D.M., "Spectral transformation of multiple-valued decision diagrams", *Proc. 24th Int. Symp. on Multiple-valued Logic*, Boston, Massachusetts, USA, May 22-25, 1994, 89-96.
- [51] Miller, D.M., "An improved method for computing a generalized spectral coefficient", *IEEE Trans. on CAD*, Vol. 17, No. 3, 1998, 233-238.
- [52] Muller, D.E., "Application of Boolean algebra to switching circuits design and error detection", *IRE Trans. Electron. Comput.*, EC-3, 1954, 6-12.
- [53] Moraga, C., *Theory and Applications of Spectral Techniques*, Forschungsbereich 268, FB Informatik, Universitaet Dortmund, 1988, ISSN 0933-6192.
- [54] Moraga, C., "A decade of spectral techniques", *Proc. 21st Int. Symp. on Multiple-Valued Logic*, Victoria, Canada, May 1991, 182-188.
- [55] Moraga, C., "Spectral techniques and soft computing", *Proc. 5th Int. Symp. on Spectral Techniques*, March 15-17, Beijing, China, 1994, 1-7.
- [56] Moraga, C., *Advances in Spectral Techniques*, Berichte zur angewandten Informatik, Dortmund, 1998.2, ISSN 0946-2341.
- [57] Moraga, C., Heider, R., "Tutorial review on applications of the Walsh transform in switching theory", *Proc. First Int. Workshop on Transforms and Filter Banks*, TICSP Series # 1, June 1998, 494-512.
- [58] Oenning, R., Moraga, C., "Properties of the Zhang-Watari transform", *Proc. 25th Int. Symp. on Multiple-Valued Logic*, May 23-25, 1995, 44-49.
- [59] Perkowski, M.A., "The generalized orthonormal expansions of functions with multiple-valued input and some of its applications", *Proc. Int. Symp. on Multiple-Valued Logic*, May 1992, Sendai, Japan, 442-450.
- [60] Picton, P.D., "Higher order neural networks and the arithmetic transform", *Proc. Second Int. Conf. on Artificial Neural Networks*, 1991, 290-294.
- [61] Rahardja, S., Falkowski, B.J., "Family of Complex Hadamard transforms: Relationships with other transforms and Complex Composite spectra", *Proc. IEEE Int. Symp. on Multiple-Valued Logic*, Antigonish, Nova Scotia, Canada, May 1997, 125-130.
- [62] Ray Liy, K.J., "VLSI computing architectures for Haar transform", *Electronics Letters*, Vol. 26, No. 23, 1990, 1962-1963.
- [63] Reed, I.S., "A class of multiple-error-correcting circuits and their decoding scheme", *IRE Trans. Inform. Theory*, PGIT-4, 1954, 38-49.
- [64] Roziner, T., Karpovsky, M.G., "Multidimensional Fourier transforms by systolic architectures," *Journal of VLSI Signal Processing*, No. 4, 1992, 343-354.
- [65] Ruiz, G., Michell, J.A., Buron, A., "Switch-level fault detection and diagnosis environment for MOS digital circuits using spectral techniques", *IEE Proc.*, Part E, Vol. 139, No. 4, July 1992, 293-307.
- [66] Sasao, T., "AND-EXOR expressions and their optimizations", in: [67], 287-312.
- [67] Sasao, T., (ed.), *Logic Synthesis and Optimization*, Kluwer Academic Publishers, 1993.
- [68] Sasao, T., "Representations of logic functions by using EXOR operators", in: [72], 29-54.
- [69] Sasao, T., *Switching Theory for Logic Synthesis*, Kluwer Academic Publishers, 1999.
- [70] Sasao, T., Besslich, Ph.W., "On the complexity of MOD-2 sum PLA's", *IEEE Trans. Comput.*, Vol.33, No.2, 1990, 262-266.
- [71] Sasao, T., Debnath, D., "An exact minimization algorithm for generalized Reed-Muller expansions", *Proc. IEEE Asia-Pacific Conference on Circuits and Systems*, December, 5-8, 1994, Taipei, Taiwan, 460-465.
- [72] Sasao, T., Fujita, M., (eds.), *Representations of Discrete Functions*, Kluwer Academic Publishers, 1996.
- [73] Shmerko, V.P., Mikhailov, S.V., "Review of publications in the former Soviet Union on spectral methods of logic data processing and logic differential calculus", *Proc. 5th Int. Workshop on Spectral Techniques*, Beijing, China, March 15-17, 1994, 48-54.
- [74] Stanković, M., Janković, D., Stanković, R.S., "Efficient algorithms for Haar spectrum calculation", *Scientific Review*, No. 21-22, 1996, 171-182.

- [75] Stanković, R.S., "Some remarks about spectral transform interpretation of MTBDDs and EVBDDs," *ASP-DAC'95*, Makuhari Messe, Chiba, Japan, August 29-September 1, 1995, 385-390.
- [76] Stanković, R.S., "Reed-Muller expressions and Walsh transform", *Avtomatika i Telemekhanika*, No. 4, 1996, 130-147, (in Russian).
- [77] Stanković, R.S., "Fourier decision diagrams on finite non-Abelian groups with preprocessing", *Proc. 27th Int. Symp. on Multiple-Valued Logic*, May 28-30, 1997, Antigonish, Nova Scotia, Canada, 281-286.
- [78] Stanković, R.S., *Spectral Transform Decision Diagrams in Simple Questions and Simple Answers*, Nauka, Belgrade, 1998.
- [79] Stanković, R.S., "Some remarks on terminology in spectral techniques for logic design, Walsh and Hadamard matrices", *IEEE Trans. on CAD*, Vol. 17, No. 11, 1998, 1211-1214.
- [80] Stanković, R.S., Milenović, D., "Some remarks on calculation complexity of Fourier transforms on finite groups", *Proc. 14th European Meeting on Cybernetics and Systems Research, CSMR'98*, April 15-17, 1998, Vienna, Austria, 59-64.
- [81] Stanković, R.S., Milenović, D., "Some remarks on Fourier transforms on finite non-Abelian groups", *Proc. First Int. Conf. on Information, Communications and Signal Processing*, Singapore, September 9-12, 1997.
- [82] Stanković, R.S., Moraga, C., Astola, J.T., *Readings in Fourier Analysis on Finite Non-Abelian Groups*, TICSP Series #5, TICSP, Finland, 1999.
- [83] Stanković, R.S., Moraga, C., Sasao, T., "A unifying approach to edge-valued and arithmetic transform decision diagrams", Research Report 736, Dortmund University, Dortmund, Germany, 2000.
- [84] Stanković, R.S., Sasao, T., "Decision diagrams for representation of discrete functions: uniform interpretation and classification", *Proc. ASP-DAC'98*, Yokohama, Japan, February 13-17, 1998.
- [85] Stanković, R.S., Sasao, T., Moraga, C., "Spectral transform decision diagrams" in: [72], 55-92.
- [86] Stanković, R.S., Stanković, M., "Calculation of Gibbs derivatives on finite Abelian groups through decision diagrams", *Journal of Approximation Theory and Its Applications*, Vol. 14, No. 9, 1998, 12-25.
- [87] Stanković, R.S., Stanković, M., Astola, J.T., Egiazarian, K., "Fibonacci decision diagrams and spectral Fibonacci decision diagrams", *Proc. 30th Int. Symp. on Multiple-Valued Logic*, Portland, Oregon, USA, May 23-25, 206-211.
- [88] Stanković, M., Stanković, R.S., Astola, J.T., Egiazarian, K., *Fibonacci Decision Diagrams*, TICSP Series # 8, TICSP, Finland, 2000.
- [89] Stanković, R.S., Stanković, M., Janković, D., *Spectral Transforms in Switching Theory, Definitions and Calculations*, Nauka, Belgrade, 1998.
- [90] Stanković, R.S., Stojić, M.R., Stanković, M.S., *Recent Developments in Abstract Harmonic Analysis with Applications in Signal Processing*, Nauka, Belgrade and Elektronski fakultet, Niš, 1996.
- [91] *The Annals of the Computation Laboratory of Harvard University*, Volume XXVII, *Synthesis of Electronic Computing and Control Circuits*, Cambridge, Massachusetts, 1951.
- [92] Thornton, M.A., "Modified Haar transform calculation using digital circuit output probabilities", *Proc. IEEE Int. Conf. on Information, Communications and Signal Processing* (1st ICICS), Singapore, Vol. 1, September 1997, 52-58.
- [93] Thornton, M.A., Drechsler, R., Günther, "Probabilistic equivalence checking using partial Haar spectral diagrams", *Proc. 4th Int. Workshop on Applications of the Reed-Muller Expansion in Circuit Design*, Victoria, Canada, August 20-21, 1999, 123-132.
- [94] Trachtenberg, E.A., "Applications of Fourier Analysis on Groups in Engineering Practices", in: [90], 331-403.
- [95] Varma, D., Trachtenberg, E.A., "Efficient spectral techniques for logic synthesis", in: T. Sasao, (ed.), *Logic Synthesis and Optimization*, Kluwer Academic Publishers, Boston, 1993, 215-232.
- [96] Vrudhula, S.B.K., Pedram, M., Lai, Y.-T., "Edge valued binary decision diagrams", in: Sasao, T., Fujita, M., (ed.), *Representations of Discrete Functions*, Kluwer Academic Publishers, 1996, 109-132.
- [97] Yang, J.C.-Y., De Michelli, G., "Spectral techniques for technology mapping", Tech. Rept. Stanford.cs/CSL-TR-91-498, Stanford University, 1991.
- [98] Yanushkevich, S.N., "Developing Boolean differential calculus methods for arithmetical logic", *Automation and Remote Control*, Vol. 55, No. 5, Part 2, 1994, 715-729.
- [99] Yanushkevich, S.N., "Spectral and differential methods to synthesize polynomial forms of MVL functions on systolic arrays", *Proc. 5th Int. Symp. on Spectral Techniques*, Beijing, China, March 15-17, 78-93.
- [100] Yanushkevich, S.N., "Arithmetical canonical expressions of Boolean and MVL functions as generalized Reed-Muller series", *Proc. IFIP WG10.5 Workshop in Applications of the Reed-Muller Expression in Circuit Design*, Chiba, Japan, 1995, 300-307.
- [101] Yanushkevich, S.N., *Logic Differential Calculus in Multi-Valued Logic Design*, Techn. University of Szczecin Academic Publisher, Poland, 1998.
- [102] Zhang, Q.S., Zhang, Y.G., *Theory and Applications of Bridge Functions*, Defense Industry Publisher, Beijing, 1992, (in Mandarin).
- [103] Zhang, Q.S., *New Methods of Signal Information Transfer*, Aeronautics and Astronautics Publishers, Beijing, 1989, (in Mandarin).
- [104] Zhegalkin, I.I., "On the techniques of calculating sentences in symbolic logic", *Math. Sb.*, Vol. 34, 1927, 9-28, (in Russian).